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Date : August 8, 2005
TO : Director, Office of Patent Publication
FROM : Office of the Deputy Commissioner
for Patent Examination Policy
SUBJECT : Withdrawal from Issue of **Application No. 10/805,822**

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Applicant(s) : Makoto Yamamoto et al
Application No. : 10/805,822
Filed : March 22, 2004

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The above-identified application has been assigned Patent No. 6,927,115 and an issue date of August 9, 2005. **OFFICE OF PETITIONS**

It is hereby directed that this application be withdrawn from issue at the request of the applicant. Do not refund the issue fee.

The following erratum should be published in the Official Gazette if the above-identified application is published in the OG of August 9, 2005:

"All reference to Patent No. 6,927,115 to Makoto Yamamoto et al of Japan for METHOD OF FABRICATING SEMICONDUCTOR INTEGRATED CIRCUIT appearing in the Official Gazette of August 9, 2005 should be deleted since no patent was granted."

Frances Hicks

Frances Hicks
Petitions Examiner
Office of Petitions
Office of the Deputy Commissioner
for Patent Examination Policy

cc: Paul Harrison; Crystal Park 3, Suite 441 (FAX-703-306-2737)
Deneise Boyd, Crystal Park 2, Suite 1100 (FAX-308-5413)
Mary Louise McAskill, Crystal Park 3-910 (FAX 305-4372)
Niomi Farmer, Crystal Park 3-910 (FAX-305-4372)
Mary E. Johnson (Cookie), P/OCS, CM1-6D07
Duane Davis (CDS), CM1-6A07
Tamara K. Greene, PK3-910